

Program: BE-Electrical- Engineering

Curriculum Scheme: Revised 2016

Examination: Third Year Semester VI

Course Code: \_EEC604\_ and Course Name: \_Microcontroller and its Applications (MCA)\_\_\_

Time: 1 hour

Max. Marks: 50

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Note to the students:- All the Questions are compulsory and carry equal marks .

Q1.	PIC 18 is ____ bit micro controller.
Option A:	16
Option B:	32
Option C:	8
Option D:	24
Q2.	The maximum number of instruction cycles needed to execute a PIC 18 instruction is ____.
Option A:	3
Option B:	6
Option C:	8
Option D:	1
Q3.	Give the names of the buses present in a controller for transferring data from one place to another.
Option A:	data bus, address bus
Option B:	data bus
Option C:	data bus, address bus, control bus
Option D:	address bus
Q4.	What is the order decided by a processor or the CPU of a controller to execute an instruction?
Option A:	decode, fetch, execute
Option B:	execute, fetch, decode
Option C:	fetch, execute, decode
Option D:	fetch, decode, execute
Q5.	The literal value in MOVLW is ----- bits wide.
Option A:	16
Option B:	24
Option C:	8
Option D:	32

Q6.	The largest number that K can take for the instruction ADDLW K is ---- in hex.
Option A:	FFFFH
Option B:	FFH
Option C:	FFFH
Option D:	FFFFFH
Q7.	The access bank has ----- bytes space.
Option A:	128
Option B:	64
Option C:	956
Option D:	256
Q8.	The result obtained when the following instructions are executed: MOVLW F5H, ADDLW 0BH
Option A:	10H
Option B:	11H
Option C:	01H
Option D:	00H
Q9.	The INDF instruction is used with ----- Addressing mode
Option A:	Direct
Option B:	indirect
Option C:	Immediate
Option D:	Register
Q10.	What does the instruction BRA mean?
Option A:	Branch Unconditionally
Option B:	Branch if Not Zero
Option C:	Branch if Overflow
Option D:	Branch if No overflow
Q11.	In multiplication of two bytes in PIC18, we can place one byte in _____ register and for the other one we can use value K.
Option A:	fileReg
Option B:	WReg
Option C:	Dreg
Option D:	PRCF
Q12.	_____ SFR is used solely for the purpose of making a given port an input or output port.
Option A:	TRISX
Option B:	LATX
Option C:	PORTX
Option D:	LAAX
Q13.	TRIS stands for

Option A:	TRIstate
Option B:	TRIstatus
Option C:	TRIsymbol
Option D:	TRIsystem
Q14.	TXSTA is how many bit register
Option A:	2
Option B:	4
Option C:	6
Option D:	8
Q15.	Select a data range of unsigned int.
Option A:	0 to 255
Option B:	0 to 127
Option C:	0 to 65535
Option D:	0 to 16777215
Q16.	The signed char is _____ data type in size
Option A:	24-bit
Option B:	32-bit
Option C:	16-bit
Option D:	8-bit
Q17.	Select a wrong statement from the following
Option A:	It is easier and less time consuming to write in Assembly than C
Option B:	C is easier to modify and update
Option C:	You can use code available in function libraries
Option D:	C code is portable to other microcontrollers with little or no modification
Q18.	In PIC 18 microcontroller, in 16-bit mode, the counter rolls over when the counter goes from _____ to _____.
Option A:	FFFFH to 0000H
Option B:	0FFFH to 0000H
Option C:	FFH to 00H
Option D:	1FFH to 000H
Q19.	Which bit permits to enable (if set) or disable (if cleared) all the interrupts in an INTCON register?
Option A:	GIE
Option B:	ADIE
Option C:	RBIE
Option D:	TOIE
Q20.	Timer 0 of PIC18 supports the highest pre scale value of _____.
Option A:	16
Option B:	8

Option C:	256
Option D:	64
Q21.	For the LCD to recognize information at the data pins as data, ____ pin must be set to high.
Option A:	E
Option B:	RS
Option C:	IN
Option D:	RB0
Q22.	To detect which row the key press belongs to , the microcontroller grounds ____ at a time.
Option A:	one row
Option B:	All rows
Option C:	All columns
Option D:	All rows one column
Q23.	To identify the key press, microcontroller rotates column bits into _____ flag.
Option A:	negative
Option B:	DC
Option C:	Zero
Option D:	carry
Q24.	Which pins of PIC 18 are used for PWM?
Option A:	RD4-RD7
Option B:	RD3-RD0
Option C:	RC0-RC3
Option D:	RB3-RB0
Q25.	Which bit is mandatory to get initiated or set for executing the process of analog to digital conversion in ADCON0?
Option A:	ADIF
Option B:	ADON
Option C:	Go!/Done
Option D:	ADSC1

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Question	Correct Option (Enter either 'A' or 'B' or 'C' or 'D')
Q1.	C
Q2.	A
Q3.	C
Q4	D
Q5	C
Q6	B
Q7	D
Q8.	D
Q9.	B
Q10.	A
Q11.	B
Q12.	A
Q13.	A
Q14.	D
Q15.	C
Q16.	D

Q17.	A
Q18.	A
Q19.	A
Q20.	C
Q21.	B
Q22.	A
Q23.	D
Q24.	A
Q25.	C