University of Mumbai

Examination 2020 under cluster 4(PCE)

Program: BE Electronics and Telecommunication Engineering
Curriculum Scheme: Rev2016
Examination: Third Year Semester VI
Course Code: ECC 601 and Course Name: MCA

Time: 1 hour   Max. Marks: 50

Note to the students: - All the Questions are compulsory and carry equal marks .

Q1.	Which bit must be set in TCON register in order to start the 'Timer 0' while operating in 'Mode 0'?
Option A:	TR0
Option B:	TF0
Option C:	IT0
Option D:	IE0
Q2.	Normal operating mode for most programs (tasks) means ARM7 is in which mode?
Option A:	FIQ mode
Option B:	System Mode
Option C:	Abort Mode
Option D:	User Mode
Q3.	Which control line/s act/s as an initiator by apprising LCD about the inception of data transmission by the microcontroller?
Option A:	Enable (EN)
Option B:	Register Select (RS)
Option C:	Read/Write (RW)
Option D:	Data line (D0-D7)
Q4.	Which of the following is used for multiplication in ARM7
Option A:	AND
Option B:	MUL
Option C:	ADC
Option D:	ADDS
Q5.	The register that can be used as scratch pad is :
Option A:	Data Register
Option B:	Accumulator & B Register
Option C:	Accumulator
Option D:	B register
Q6.	In ARM7 instruction AND R0,R1,R2 performs
Option A:	R0=R1 AND R2
Option B:	R0=R0 AND R1
Option C:	R1=R0 AND R1
Option D:	R2=R0 AND R1
Q7.	How many ports are there in 8051 controller?

Option A:	Five
Option B:	Four
Option C:	Three
Option D:	Two
Q8.	The RISC stands for _____
Option A:	Restricted Instruction Set Computer
Option B:	Regular Instruction Set Computer
Option C:	Reduced Instruction Set Computer
Option D:	Reduced Induction Set Computer
Q9.	The register that can provide control and status information about serial port is
Option A:	PCON and SCON
Option B:	TSCOM
Option C:	IE
Option D:	IP
Q10.	What kind of instructions usually affect the program counter?
Option A:	Call & Jump
Option B:	Call & Return
Option C:	Push & Pop
Option D:	Return & Jump
Q11.	The instruction that supports addition when carry exists is
Option A:	ADD
Option B:	ADC
Option C:	OR
Option D:	XOR
Q12.	When the Microcontroller executes arithmetic operations , the flag bits of which register are affected ?
Option A:	PC
Option B:	DPTR
Option C:	PSW
Option D:	SP
Q13.	Which of the following instruction is example of Direct addressing mode
Option A:	MOVA,@R0
Option B:	MOVR0,#10H
Option C:	MOV10H,A
Option D:	MOVR5,A
Q14.	Interfacing LCD with 89C51 _____ data lines are used along with the _____ signals.
Option A:	6, RS, RW
Option B:	5, RW, EN
Option C:	8, RS, EN, RW
Option D:	9, RS, EN, RW

Q15.	The_____ uses a set of translation tables to provide fine-grained control over memory.
Option A:	Memory Management Unit
Option B:	Memory Protection Unit
Option C:	Segment Unit
Option D:	Status Unit
Q16.	How many bit Data Bus ARM7 has?
Option A:	8-bit
Option B:	16-bit
Option C:	32-bit
Option D:	64-bit
Q17.	The register that can be used as an operand register is
Option A:	Data Register
Option B:	Accumulator and B Register
Option C:	Accumulator
Option D:	B register
Q18.	In embedded programming for continuous operation we execute while loop by passing in _____ in bracket followed.
Option A:	1
Option B:	Null
Option C:	Infinity
Option D:	0
Q19.	LCALL instruction takes (M)
Option A:	2 bytes
Option B:	4 bytes
Option C:	3 bytes
Option D:	1 bytes
Q20.	An alternate function of port pin P3.4 in the 8051 Microcontroller
Option A:	Interrupt 1
Option B:	Interrupt 0
Option C:	Timer 1
Option D:	Timer 0
Q21.	In ARM7 software interrupt instruction is
Option A:	INTR
Option B:	SWI
Option C:	SI
Option D:	INT
Q22.	How is memory accessed in RISC architecture?
Option A:	using memory instructions
Option B:	using load and store instructions

Option C:	using opcode instructions
Option D:	using bus instructions
Q23.	Which of the following is used for POP operation in ARM7
Option A:	LDMFA
Option B:	STMFA
Option C:	STMED
Option D:	STMEA
Q24.	Which of the following statement is true?
Option A:	Microcontroller is a chip on board
Option B:	Microcontroller is a part of Microprocessor
Option C:	Microprocessor is a part of Microcontroller
Option D:	Microprocessor is the advanced version of Microcontroller
Q25.	IF DPTR=20FFH, the contents of DPH after the execution of instruction INC DPTR will be
Option A:	20H
Option B:	21H
Option C:	00H
Option D:	FFH

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Question	Correct Option (Enter either 'A' or 'B' or 'C' or 'D')
Q1.	A
Q2.	D
Q3.	A
Q4	B
Q5	D
Q6	A
Q7	B
Q8.	C
Q9.	A
Q10.	A
Q11.	B
Q12.	C
Q13.	C
Q14.	C
Q15.	A
Q16.	C
Q17.	B
Q18.	A
Q19.	C
Q20.	D
Q21.	B
Q22.	B
Q23.	A
Q24.	A
Q25.	B