

# University of Mumbai

## Examination 2020 under cluster 4 (PCE)

Program: BE Electronics & Telecommunication Engineering

Curriculum Scheme: Rev2016

Examination: Third Year Semester V

Course Code: **ECC501** and Course Name: **Microprocessors & Peripherals**

Time: 1 hour

Max. Marks: 50

Note to the students:- All the Questions are compulsory and carry equal marks .

Q1.	_____ is used to locate instructions in a proper sequence of a program.
Option A:	Address bus
Option B:	Data bus
Option C:	Program Counter or Instruction Pointer
Option D:	Memory
Q2.	The register in which instruction opcode is stored temporarily is called as _____.
Option A:	Instruction Register.
Option B:	Instruction Decoder.
Option C:	Program Counter.
Option D:	Accumulator
Q3.	The combination of opcode and an operand, that can be used to instruct a system, is called as _____.
Option A:	Memory location.
Option B:	Data
Option C:	Instruction
Option D:	Memory
Q4.	If MN/MX' pin of 8086 is connected to ground
Option A:	8086 operates in Minimum mode
Option B:	8086 operates in Maximum mode
Option C:	8086 operates idle mode
Option D:	8086 wait for result from 8087
Q5.	INTA' pin of 8086 is _____.
Option A:	Interrupt acknowledgement for NMI interrupt
Option B:	Interrupt acknowledgement for INTR interrupt
Option C:	Interrupt acknowledgement for both NMI and INTR interrupts
Option D:	Interrupt acknowledgment for divide by zero interrupt
Q6.	8086 floats its local bus and control lines when
Option A:	HOLD pin goes high
Option B:	TEST pin goes low
Option C:	READY goes high
Option D:	LOCK pin goes high
Q7.	DT/R' and DEN' pin of 8086 microprocessor is used for
Option A:	clock generator 8284 to generate clock

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Option B:	octal latch 8282 to separate address & data bus
Option C:	Transceiver 8286 to increase driving capacity of data bus.
Option D:	decoder 74138 to generate control signals.
Q8.	In 8086 Microprocessor, which flag will not be affected in case of INC and DEC instructions?
Option A:	Carry
Option B:	Sign
Option C:	Zero
Option D:	Parity
Q9.	In 8086 Microprocessor, OUT 24H, AL instruction belongs to _____ addressing mode.
Option A:	Direct Port
Option B:	Indirect Port
Option C:	Direct
Option D:	String
Q10.	In 8086 Microprocessor, for Intersegment branching, _____ will change
Option A:	only IP
Option B:	only CS
Option C:	CS and IP both
Option D:	Accumulator
Q11.	Opcode is not generated for any _____ written in program for 8086 microprocessor.
Option A:	assembler directives
Option B:	instruction
Option C:	operation
Option D:	program
Q12.	For mix language programming of 8086 Microprocessor, instructions written in assembly language must be started with _____
Option A:	asm keyword
Option B:	C keyword
Option C:	code segment
Option D:	data segment
Q13.	In 8254 Programmable timer, The generation of a square wave is possible in
Option A:	Mode 1
Option B:	Mode 2
Option C:	Mode 4
Option D:	Mode 3
Q14.	The number of counters that are present in the programmable timer device 8254 is
Option A:	1
Option B:	2
Option C:	3

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Option D:	4
Q15.	In BSR mode, only port C can be used to
Option A:	set individual bits of port C
Option B:	reset individual bits of port C
Option C:	set and reset individual bits of port C
Option D:	programmable I/O ports
Q16.	In which mode, all the Ports of the 8255 PPI work as Input-Output units for data transfer?
Option A:	BSR Mode
Option B:	Mode 0 of I/O mode
Option C:	Mode 1 of I/O mode
Option D:	Mode 2 of I/O mode
Q17.	The NMI pin should remain high for at least
Option A:	4 clock cycles
Option B:	3 clock cycles
Option C:	1 clock cycles
Option D:	2 clock cycles
Q18.	Which is the DAC among the following?
Option A:	8086
Option B:	8085
Option C:	74373
Option D:	0808
Q19.	A binary input 000 is fed to a 3bit DAC/ADC. The resultant output is 101. Find the type of error?
Option A:	Settling error
Option B:	Gain error
Option C:	Offset error
Option D:	Linearity error
Q20.	Resolution of ADC 0808/0809 is
Option A:	16 Bit Resolution
Option B:	08 Bit Resolution
Option C:	32Bit Resolution
Option D:	04Bit Resolution
Q21.	The digital outputs of ADC $2^{-1}$ to $2^{-8}$ represents
Option A:	MSB to LSB
Option B:	LSB to MSB
Option C:	MSB only
Option D:	LSB only
Q22.	If the result is infinity, then the exception generated is
Option A:	Overflow

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Option B:	Invalid operation
Option C:	Denormalized operand
Option D:	Divide by zero
Q23.	One of the following signals belongs to the 8087 coprocessor is
Option A:	HOLD
Option B:	BUSY
Option C:	TEST
Option D:	NMI
Q24.	In 8086 microprocessor one of the following statements is not true.
Option A:	Coprocessor is interfaced in MAX mode
Option B:	Coprocessor is interfaced in MIN mode
Option C:	I/O can be interfaced in MAX / MIN mode
Option D:	Supports pipelining
Q25.	Why 8087 is referred to as Coprocessor? i) Because 8087 is used in parallel with main processor in a system, rather than serving as a main processor itself. ii) Because 8087 is used in serial with main processor in a system, rather than serving as a main processor itself. iii) Because main Microprocessor handles the general program execution and the 8087 handles specialized math computations.
Option A:	i & iii
Option B:	ii & iii
Option C:	iii only
Option D:	i only

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Question	Correct Option (Enter either 'A' or 'B' or 'C' or 'D')
Q1.	C
Q2.	A
Q3.	C
Q4	B
Q5	B
Q6	A
Q7	C
Q8.	A
Q9.	A
Q10.	C
Q11.	A
Q12.	A
Q13.	D
Q14.	C
Q15.	C
Q16.	B
Q17.	D
Q18.	D
Q19.	C
Q20.	B
Q21.	A
Q22.	D
Q23.	C
Q24.	B
Q25.	A