

Program: BE Electrical Engineering

Curriculum Scheme: Revised 2012

Examination: Third Year Semester VI

Course Code: EEC 605 and Course Name: Micro Controller and its Applications

Time: 1 hour

Max. Marks: 50

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Note to the students:- All the Questions are compulsory and carry equal marks .

Q1.	The PIC 18 Family uses ____ ROM and ____ RAM on chip.
Option A:	2MB, 4KB
Option B:	4MB, 2KB
Option C:	1MB, 2 KB
Option D:	4MB, 4 KB
Q2.	PIC microcontrollers are owned by which company?
Option A:	Intel
Option B:	MicroChip
Option C:	Atmel
Option D:	Zilog
Q3.	To transmit a byte of data serially it must be placed in a register ____
Option A:	RCREG
Option B:	TXSTA
Option C:	TXREG
Option D:	SPBRG
Q4.	The binary output of ADC module is held in which register?
Option A:	ADRESL and ADRESH
Option B:	ADCON0 and ADCON1
Option C:	ADRES and ADREO
Option D:	ADREIN and ADREOUT
Q5.	If ADON bit of ADCON0 register of ADC module is 1 then, _____.
Option A:	A/D conversion is completed.
Option B:	A/D conversion is in progress.
Option C:	A/D conversion is OFF.
Option D:	A/D Feature is powered up.
Q6.	ADDWF PORTB, W puts the result in ----
Option A:	PORT B

Option B:	SFR
Option C:	WREG
Option D:	File Register
Q7.	-34H' is represented in PIC18F as ----
Option A:	AA
Option B:	BB
Option C:	DD
Option D:	CC
Q8.	For PIC18 System of 4MHz, the time taken to execute the instruction MOVLW 55H is --- microseconds
Option A:	1
Option B:	0.5
Option C:	2
Option D:	4
Q9.	Every PIC18 family member wakes up at address ----- when it is powered up.
Option A:	FFFFH
Option B:	1111H
Option C:	0000H
Option D:	FF00H
Q10.	The vast majority of registers in PIC 18F are ----- bit
Option A:	8
Option B:	16
Option C:	24
Option D:	32
Q11.	Which of the following files is produced by an assembler?
Option A:	Myprog.org
Option B:	Myprog.lst
Option C:	Myprog.err
Option D:	Myprog.asm
Q12.	The access bank has ----- bytes space.
Option A:	128
Option B:	64
Option C:	256
Option D:	212

Q13.	The INDF instruction is used with ----- Addressing mode.
Option A:	Register Direct
Option B:	Register Indirect
Option C:	Immediate
Option D:	Indexed ROM
Q14.	The result obtained when the following instructions are executed: MOVLW F5H, ADDLW 0BH
Option A:	10H
Option B:	11H
Option C:	10H
Option D:	00H
Q15.	Which of the following is a bit oriented instruction?
Option A:	ADDLW 55H
Option B:	MOVLW 55H
Option C:	BSF STATUS,C
Option D:	CLRF STATUS
Q16.	The instruction "ANDLW 40H" uses ____ addressing mode.
Option A:	Register Direct
Option B:	Register Indirect
Option C:	Immediate
Option D:	Indexed ROM
Q17.	To identify the key press, microcontroller rotates column bits into _____ flag.
Option A:	Zero
Option B:	Carry
Option C:	DC
Option D:	Negative
Q18.	What is the Full form of BNOV Instruction?
Option A:	Branch Unconditionally
Option B:	Branch if Overflow
Option C:	Branch If No Overflow
Option D:	Branch if No Zero
Q19.	What is the Full form Of TBLRD*
Option A:	Table Read
Option B:	Table Read with Post Increment

Option C:	Table Read with Pre Increment
Option D:	Table Write
Q20.	To see if any key is pressed in matrix keyboard,_____.
Option A:	All columns are grounded.
Option B:	All rows are grounded.
Option C:	All rows and columns are grounded.
Option D:	All rows are connected to V _{DC} .
Q21.	To interface 4*4 matrixkeyboard with PIC, Rows are connected to,_____.
Option A:	RB0-RB3
Option B:	RC0-RC3
Option C:	RD0-RD3
Option D:	RA0-RA3
Q22.	The E pin is _____ pin for the LCD.
Option A:	Input
Option B:	Output
Option C:	Interrupt
Option D:	Exit
Q23.	When BRGH=0, the PIC18 divides Fosc/4 (Crystal Frequency) by _____ once more and uses that frequency for UART.
Option A:	4
Option B:	8
Option C:	16
Option D:	32
Q24.	BTFSS stands for
Option A:	Bit test file, skip if set
Option B:	Byte test file, skip if set
Option C:	Byte test file, skip if clear
Option D:	Bit test file, skip if clear
Q25.	The largest number that K can take for the instruction ADDLW K is ---- in hex.
Option A:	FFFH
Option B:	FFFH
Option C:	FFH
Option D:	FH

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Question	Correct Option (Enter either 'A' or 'B' or 'C' or 'D')
Q1.	A
Q2.	B
Q3.	C
Q4	A
Q5	D
Q6	C
Q7	D
Q8.	A
Q9.	C
Q10.	A
Q11.	D
Q12.	C
Q13.	B
Q14.	D
Q15.	C
Q16.	A
Q17.	B
Q18.	C

Q19.	A
Q20.	B
Q21.	A
Q22.	A
Q23.	C
Q24.	A
Q25.	C